

11046 U.S. PTO
10/081821

02/20/02

P

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081821	FILING DATE 02/20/2002	CLASS 326	SUBCLASS 70	GAU 2819	EXAMINER DON W
**APPLICANTS: Stockstad Troy;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/711,486 11/13/2000 NOW U.S. PAT 6,429,685					
FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>kl</i>				ATTORNEY DOCKET NO 130349	
TITLE : Integrated circuit and method of controlling output impedance					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM
(Attached in pocket on right inside flap)